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STATEMENT BY APPLICANT**
(Not for submission under 37 CFR 1.99)

Application Number	09633087
Filing Date	2000-08-04
First Named Inventor	Derrick R. Meyer
Art Unit	2157 2457
Examiner Name	Burgess, Barbara N.
Attorney Docket Number	5500-98300

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4	4807118			1989-02-21	Lin, et al.	
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6	5055999			1991-10-08	Frank, et al.	
7	5341483			1994-08-23	Frank, et al.	
8	5351043			1994-09-27	Hullett, et al.	

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9	5495481		1996-02-27	Duckwall		
10	5502840		1996-03-26	Barton		
11	5506961		1996-04-09	Carlson, et al.		
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15	5875485		1999-02-23	Matsumoto		
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19	5657472		1997-08-12	Van Loo, et al.		

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	2	Gustavson, David B., "The Scalable Coherent Interface and Related Standards Projects," IEEE Micro, 1992, p. 10.	<input type="checkbox"/>
	3	Cox, et al., "Adaptive Cache Coherency for Detecting Migratory Shared Data," Proc. 20th Annual Symposium on Computer Architecture, May 1993, pp. 98-108.	<input type="checkbox"/>
	4	Stenstrom, et al., "An Adaptive Cache Coherence Protocol Optimized for Migratory Sharing," Proc. 20th Annual Symposium on Computer Architecture, May 1993 IEEE, pp. 109-118.	<input type="checkbox"/>

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5	Wolf-Dietrich Weber, et al. "Analysis of Cache Invalidation Patterns in Multiprocessors", Computer Systems Laboratory, Stanford University, CA, pp. 243-256.	<input type="checkbox"/>
6	Kourosh, et al. "Two Techniques to Enhance the Performance of Memory Consistency Models," 1991 International Conference on Parallel Processing, pp. 1-10.	<input type="checkbox"/>
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9	Hagersten, et al., "Simple COMA," Ashley Saulsbury and Anders Landin Swedish Institute of Computer Science, Jul. 1993, pp. 233-259.	<input type="checkbox"/>
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11	Anderson, "The Performance of Spin Lock Alternatives for Shared-memory Multiprocessors", IEEE Transactions on Parallel and Distributed Systems, vol. 1, No. 1, Jan. 1990.	<input type="checkbox"/>

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